



Job Proposal: **Hardware/Software Developer on Trusted Computing Platform.**

Secure-IC

<http://www.secure-ic.com/>

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Context

“Secure-IC” is a major actor in the protection of electronic circuits against side-channel or fault injection attacks. Its main focused implementations are high performance FPGAs and ASICs. The developed competences are the fruit of scientific and industrial research in trusted computing led in the security laboratory of TELECOM ParisTech whose Secure-IC is a spin-off.

Missions

- Software design for cryptoprocessors tests, attacks and countermeasures validation.
- Specification and implementation of security solutions for embedded systems.
- Consulting services and analysis of customers technical demand.

Profile and skills

- Degree in electrical engineering.
- Practical experience in hardware design with VHDL or Verilog.
- Practical experience in at least one of the following areas:
 - Software design with use of C/C++, Perl or Python.
 - Development of cryptographic algorithms implementations.
- Knowledge in the area of embedded systems, cryptography and attacks.
- Oral and written English.
- Oral French.
- Team spirit and communications skills.
- Sense of responsibility and quality consciousness.

Salary

Depending on the profile and the experience, the gross salary for this job is comprized between 35 and 50 k€ per year. We guarantee an attractive 10% increase over the mean salary reported by the CNISF pool.